

Embed 2017 Release Notes

INTRODUCTION

solidThinking Embed, formerly known as VisSim Embedded, is a block diagram language for model-based embedded development. It supports UML State Charts integrated with dataflow block diagrams. This allows you to easily simulate and generate efficient fixed- and floating-point C code for embedded microprocessor targets. It provides a complete visual RTOS with on-chip peripheral drivers, unlimited pre-emptible tasks, simple interrupt handler creation, interrupt-based serial I/O queuing, and an interface to hand code.

solidThinking Embed is unique in its ability to generate highly efficient, high sample rate, low jitter target executables.



PLATFORM SUPPORT

Platform		
OS	Version	Architecture
Windows	10/8.1/7	x86_64

REQUIREMENTS

solidThinking Embed 2017 requires :

- Compiler: *Texas Instruments Code Composer Studio version 6.x.*
- Code Generation Tools for MSP430 support: *TI-CGT version 4.4.x.*

Learn more at solidThinking.com/Embed | 1 |

The following features and enhancements have been added for **solidThinking Embed 2017**:

GENERAL: INTEGRATION

Comm and Digital Power Designer add-ons	Embed 2017 now supports two additional modules (separate delivery and installation, using feature-based licenses): Comm and Digital Power Designer .
Diagram Efficiency	Display a warning message about diagram efficiency.

CODE GENERATION

Support for TI F28062F	Embed 2017 supports code generation for TI F28062F
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The following issues have been resolved for **solidThinking Embed 2017**:

GENERAL: SIMULATION

Application crashes when close all diagrams and search a block
18,000 block digital power diagram for cascaded buck/boost sim gives wrong results
Fixed-point blocks don't invoke help file
Embed crashes if pointer is used in Expression block
Layout issues in CodeGen dialog
DOC - Initial Value parameter cannot be set in TI DCM Ramp Generator block
Code generation dialog box isn't resizable
eepromReadWriteF28335.vsm generates an error during compilation
fixed point block fails to warn of need for float to scaled int convert block

CODE GENERATION

Compilation error for Max and Min blocks
Help button in dialog boxes for all Config blocks under Embedded Menu doesn't invoke Help file
Chip Select Pin Wrong for SPIB on 28069M
Codegen of large ARM core diagram hangs on attempt to boot C28 core
Target sampling rate too fast error
Embed crashes for C2407 blocks under Embedded menu
Buffer overwrite on ARM cortex debug interface

Web server diagrams fail to compile
Interrupt examples of Delfino and Concerto throws error of peripheral not found
F28069 ADC Config clock issue
Memory errors in Embedded>Examples>Web Server>web28M35H52-m3
Mismatch in the output pins of ePWM unit of F28377S when compared to datasheet